

## Electronic Acknowledgement Receipt

EFS ID:	1174142
Application Number:	10663967
Confirmation Number:	2684
Title of Invention:	Methods of fabricating integrated circuit devices having fuse structures including buffer layers
First Named Inventor:	Hyun-Chul Kim
Customer Number:	20792
Filer:	Elizabeth A. Stanek/Erin Campion
Filer Authorized By:	Elizabeth A. Stanek
Attorney Docket Number:	5649-1161
Receipt Date:	28-AUG-2006
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Application Type:	Utility
International Application Number:	

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		5649-1161_Amendment.pdf	331692	yes	9

	Multipart Description		
	Doc Desc	Start	End
	Amendment After Final	1	1
	Claims	2	6
	Applicant Arguments/Remarks Made in an Amendment	7	9

**Warnings:**

**Information:**

Total Files Size (in bytes):	331692
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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.